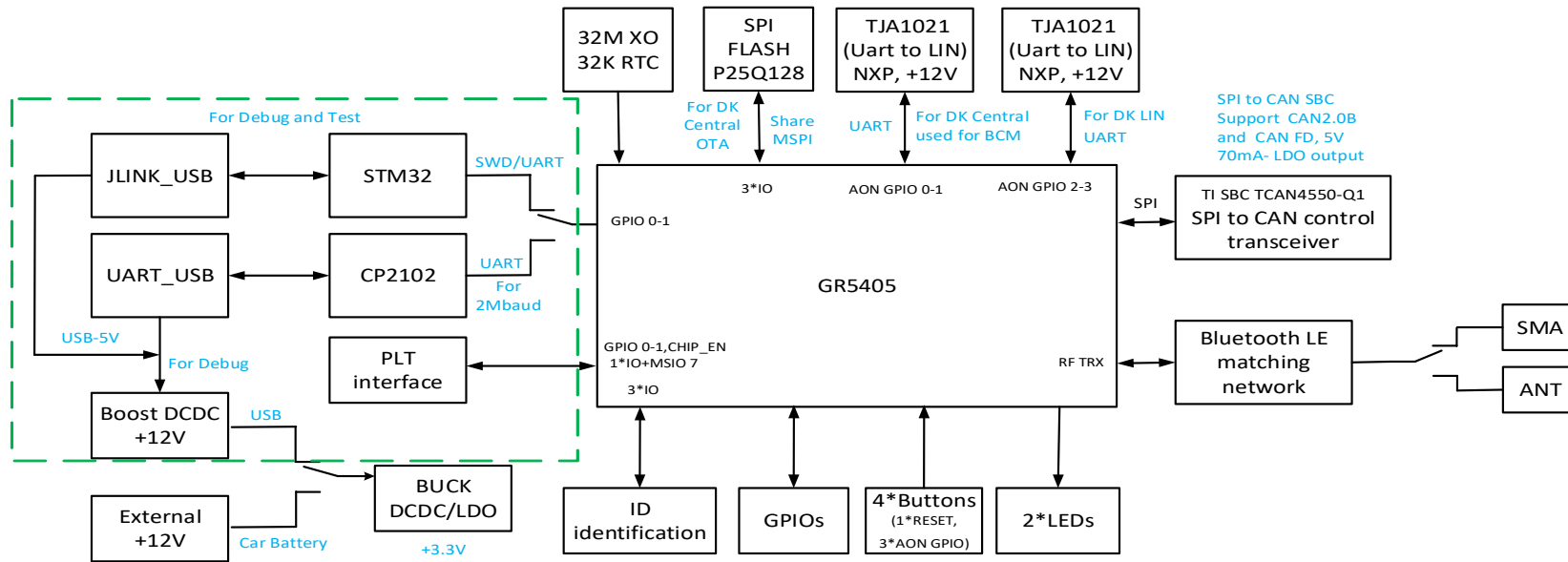
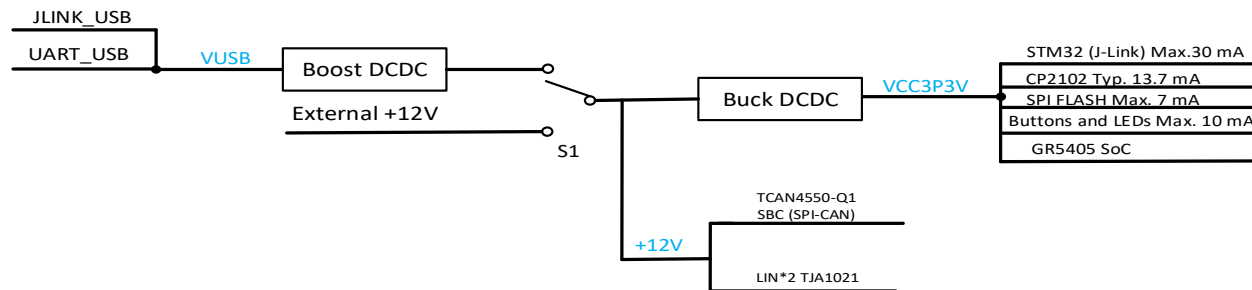


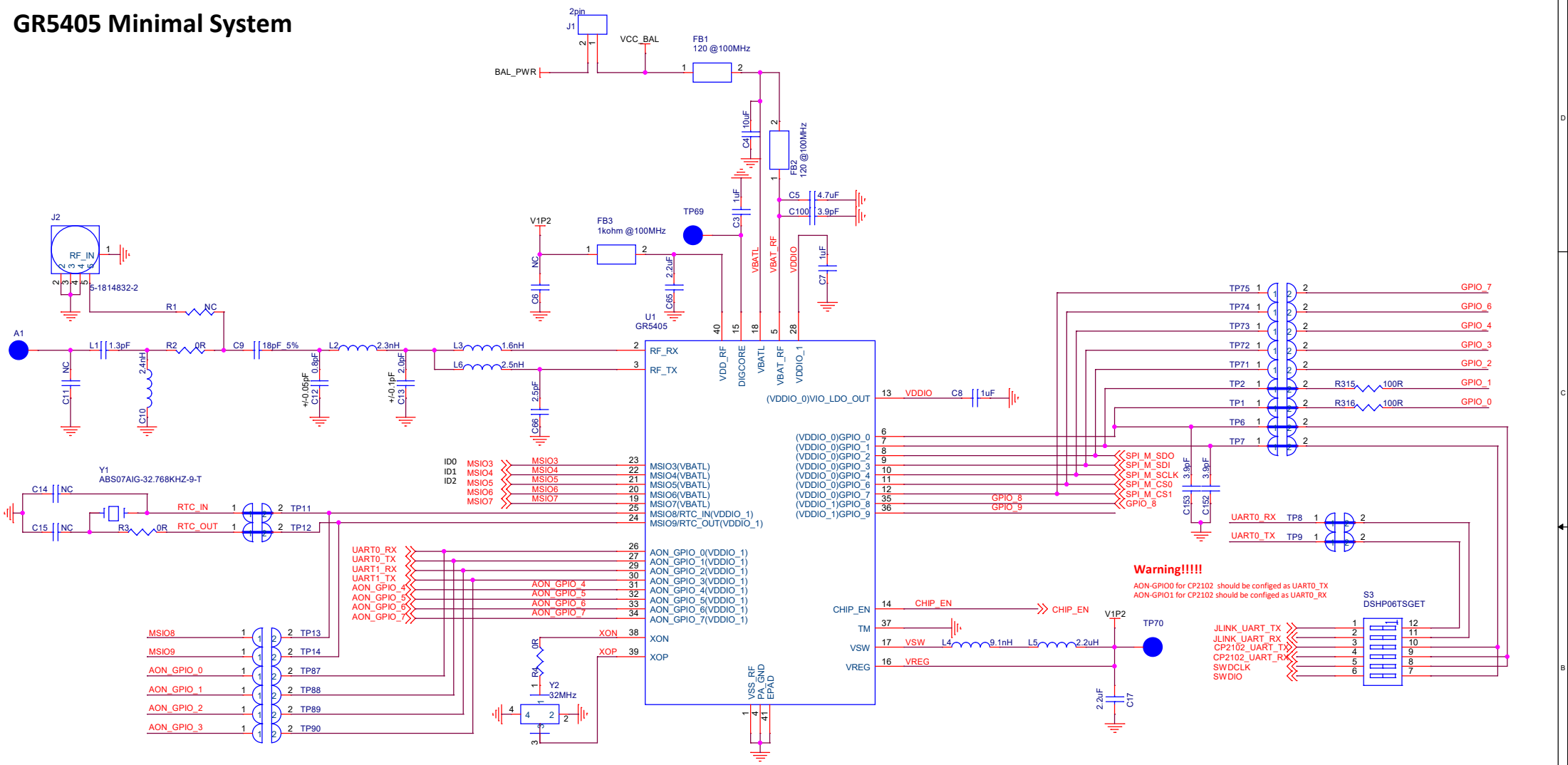
Block Diagram



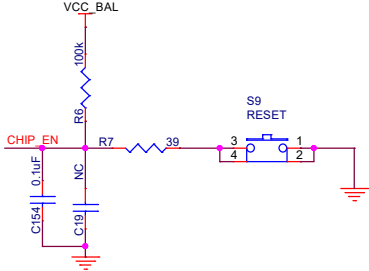
Power Tree



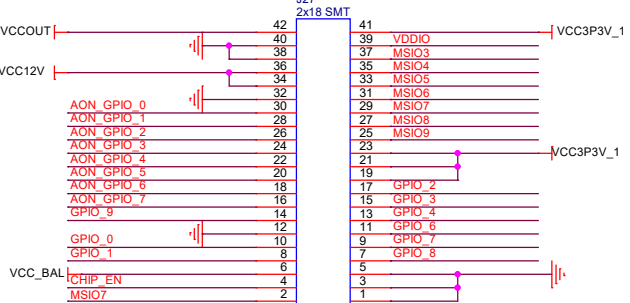
GR5405 Minimal System



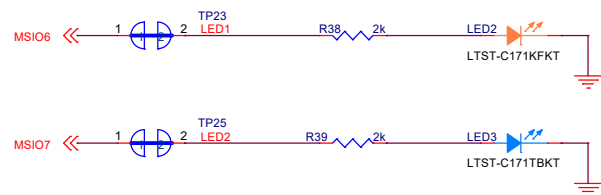
RESET



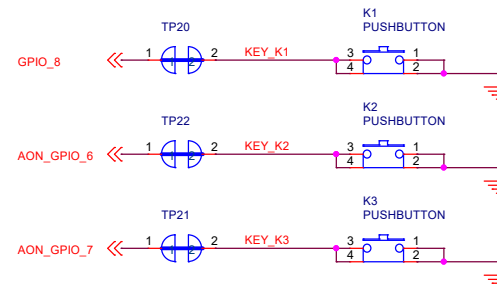
GPIOs



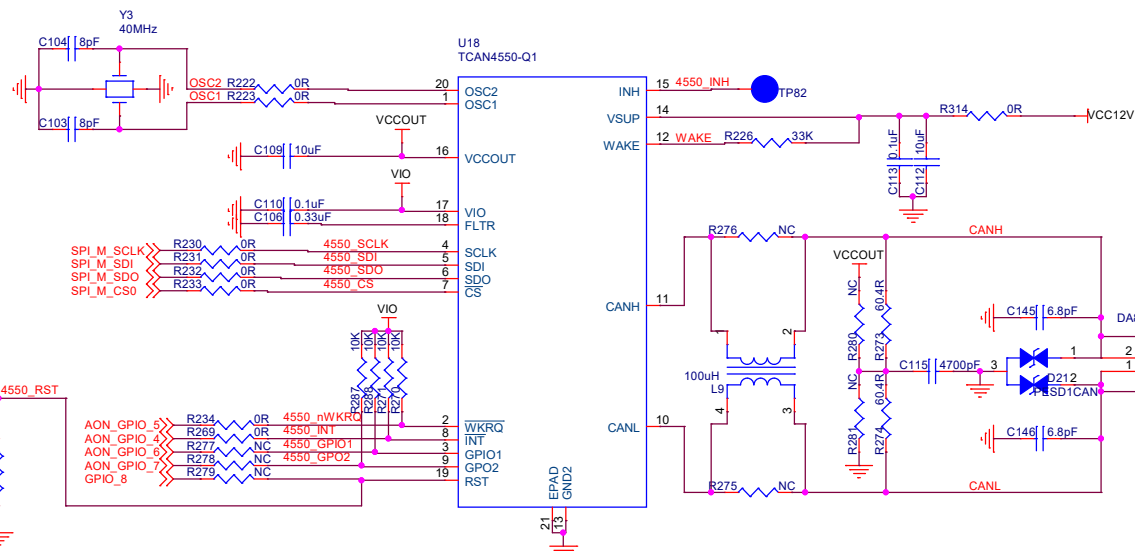
LEDs



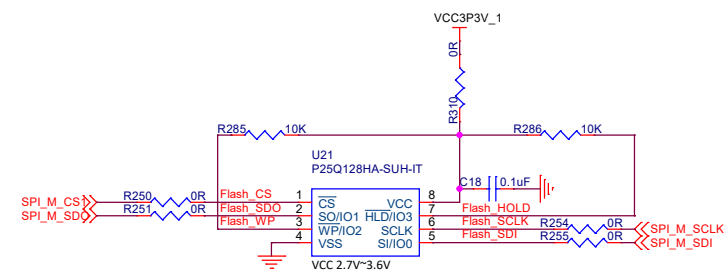
Buttons



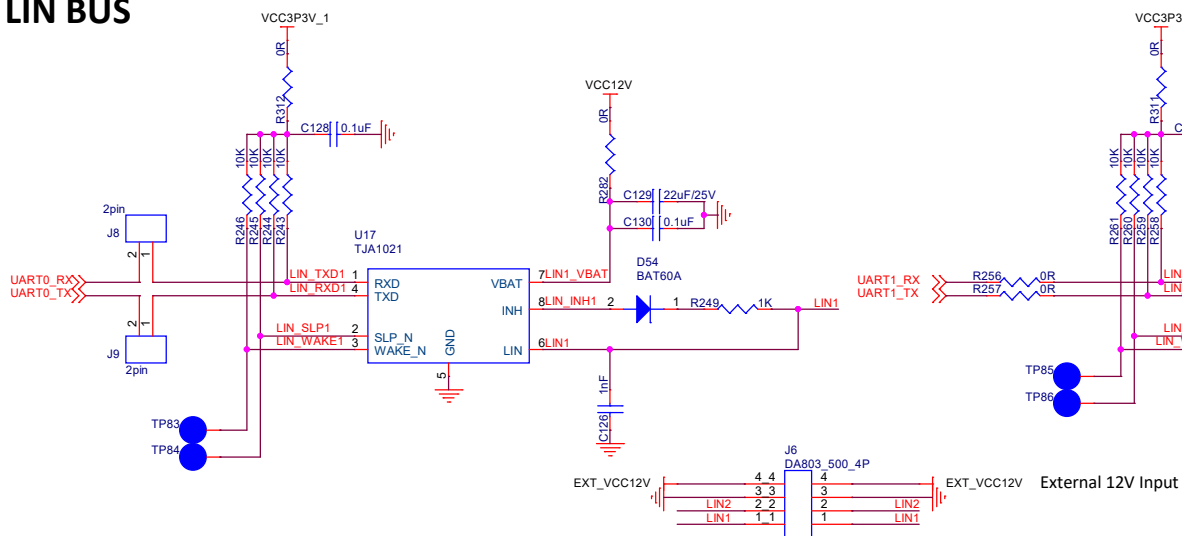
CAN FD



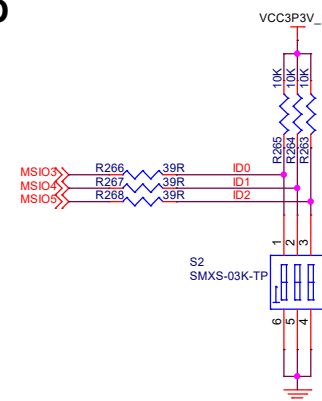
FLASH



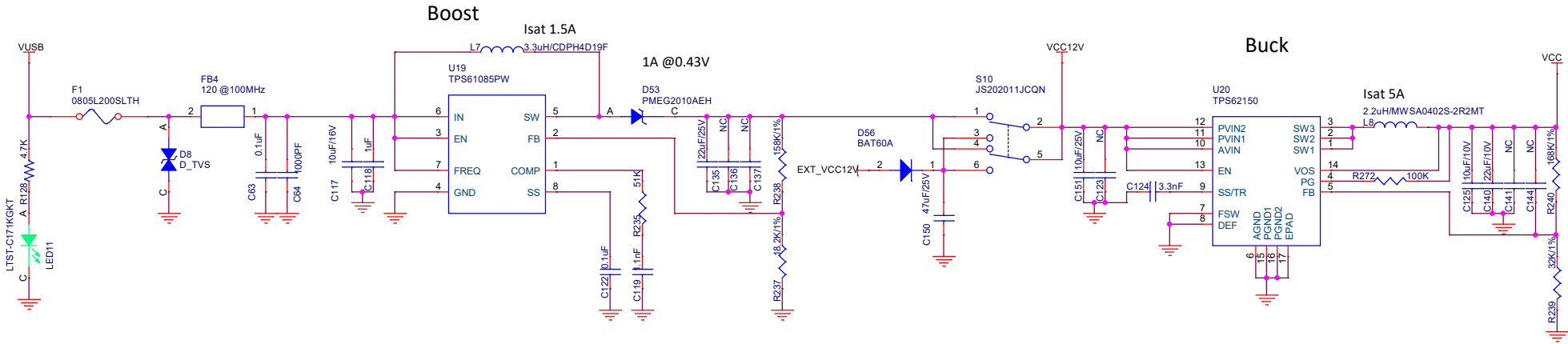
LIN BUS



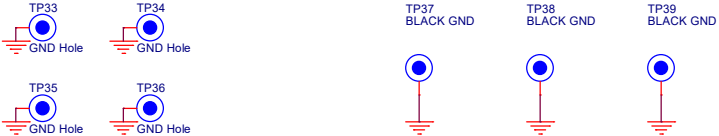
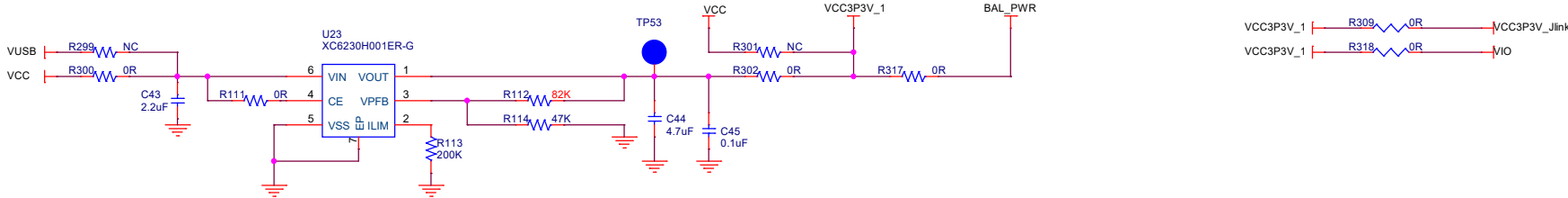
ID



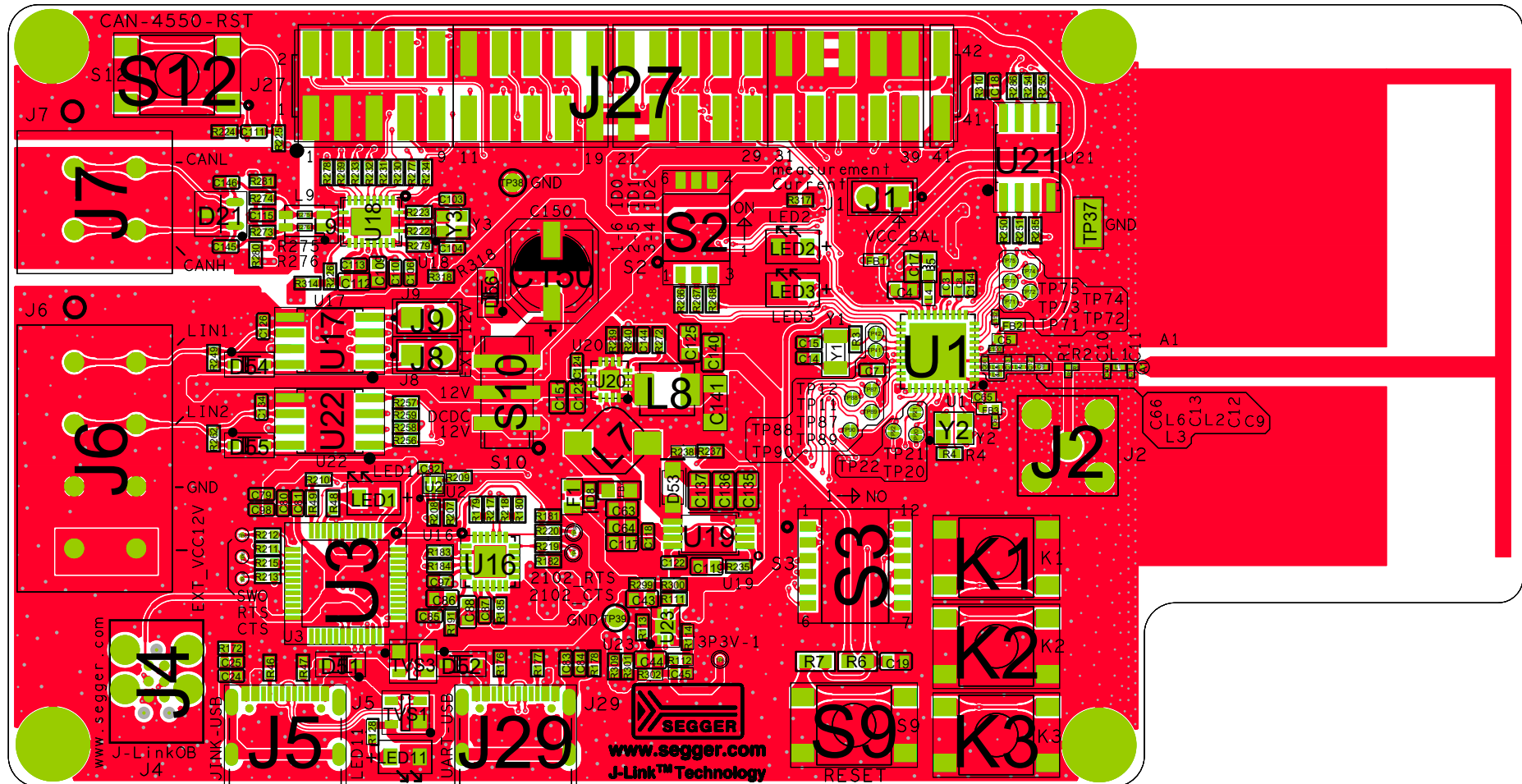
DCDC



LDO



1. Layer1 (Top)



TP33

TP36

J28

J29

J30

TP35

TP34

GOODiX

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Pinout diagram for the ATmega328P microcontroller. The diagram shows a 28-pin package with pins numbered 1 to 28. The pins are labeled as follows:

- Pin 1: Debug
- Pin 2: AIO0 J-UART_RX
- Pin 3: AIO1 J-UART_TX
- Pin 4: IO0 C-UART_RX
- Pin 5: IO1 C-UART_TX
- Pin 6: SWD
- Pin 7: SWDIO
- Pin 8: IO0 C-UART_RX
- Pin 9: IO1 C-UART_TX
- Pin 10: AIO0 J-UART_RX
- Pin 11: AIO1 J-UART_TX
- Pin 12: S3